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TITLE: IMAGE MEMORY CIRCUIT

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ABSTRACT:

PURPOSE: To obtain an economical image memory circuit by using one display screen quantity by constituting the circuit so that the start time for writing image data in a second image memory can be set arbitrarily in accordance with a read-out timing.

CONSTITUTION: The circuit is provided with a first image memory 1, and a second image memory having the memory capacity which becomes one display screen capacity at the time when it is added to the memory capacity of a first image memory 1. A timing control means 5 takes a synchronization of the read-out timing of a first image memory 1 and the write timing to a second image memory 3, and also, corrects the read-out timing and the write timing by a time base conversion. In such a way, an economical image memory circuit in which the image memory capacity is one display screen capacity can be obtained.

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Abstract Text - FPAR (2):

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